

CFD-757S/767S

SERVICE MANUAL

E Model



Photo : CFD-767S

Model Name Using Similar Mechanism	CD Section	CFD-703S	
	Tape Section	CFD-757S	CFS-W304S
		CFD-767S	NEW
Tape Transport Mechanism Type	CFD-757S	MF-757-64	
	CFD-767S	Deck A	MF-767-64PB
		Deck B	MF-767-102
Optical Pickup Block Type		KSM-360AAM	

SPECIFICATIONS

CD Player section

System
Laser diode properties

Compact disc digital audio system
Material: GaAlAs
Wave length 780 nm
Emission duration: Continuous
Laser output: Less than 44.6 μ W*
*This output is the value measured at a distance of about 200 mm from the objective lens surface on the optical pick-up block.

Spindle speed 200 r/min (rpm). to 500 r/min (rpm). (CLV)

Error correction Sony Refined Super Strategy Cross Interleave
Reed Solomon Code

Number of channels 2
Frequency response 20 - 20,000 Hz \pm 1 dB
Wow and flutter Below measurable limit

Radio section

Frequency range

	FM	MW	SW
Saudi Arabia	87.6-107 MHz		
E	87.6-108 MHz	531-1602 kHz (9 kHz STEP) 530-1710 kHz (10 kHz STEP)	(49 m) 5.85-6.35 MHz (41 m) 7.00-7.50 MHz (31 m) 9.40-9.90 MHz (25 m) 11.60-12.10 MHz (19 m) 15.10-15.60 MHz (16 m) 17.55-18.05 MHz (13 m) 21.40-21.90 MHz

IF	FM	SW/MW
	10.7 MHz	450 kHz

Antennas

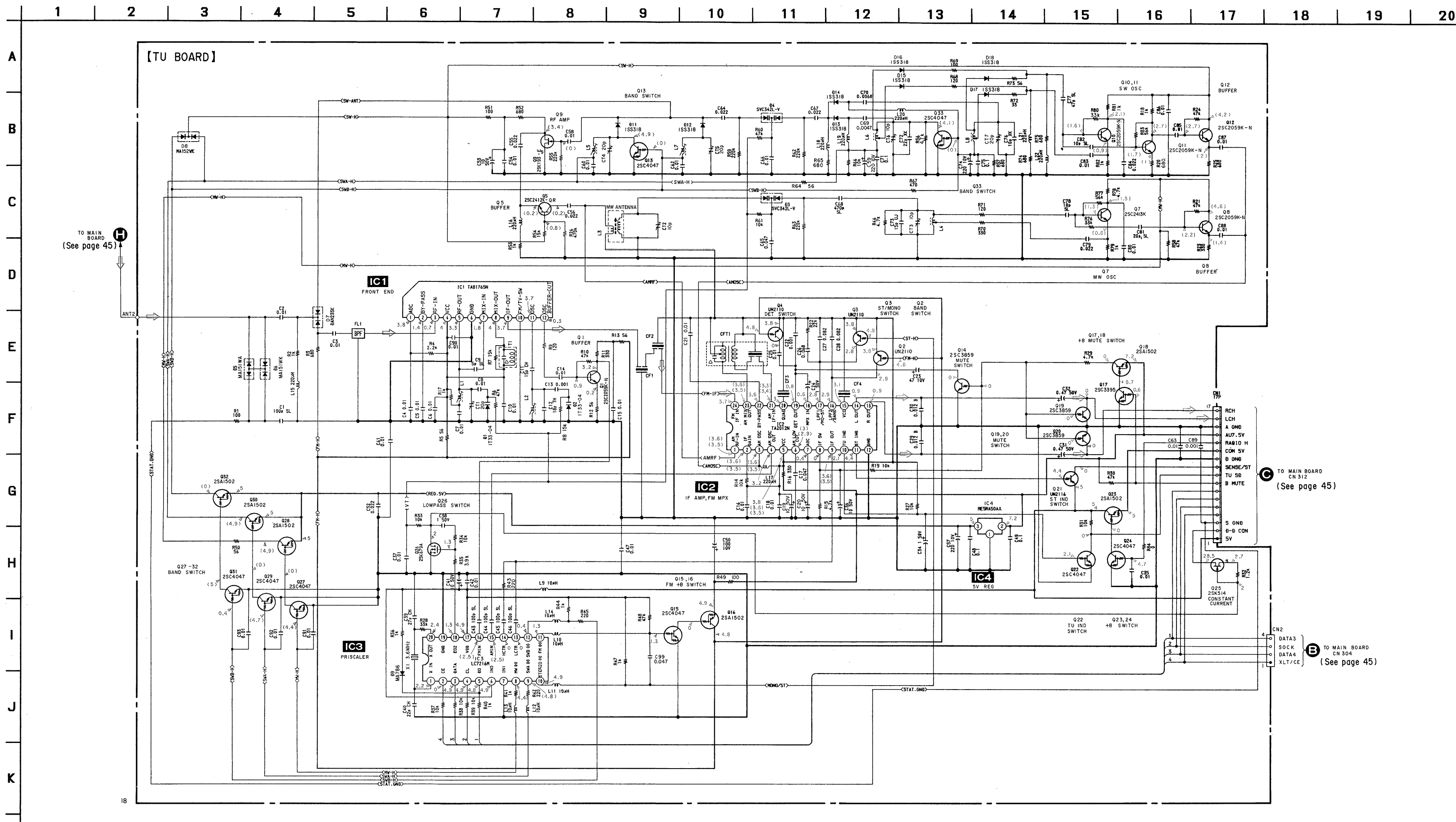
FM/SW: Telescopic antenna
MW: Built-in ferrite bar antenna

—Continued next page—

CD RADIO CASSETTE-CORDER
SONY®

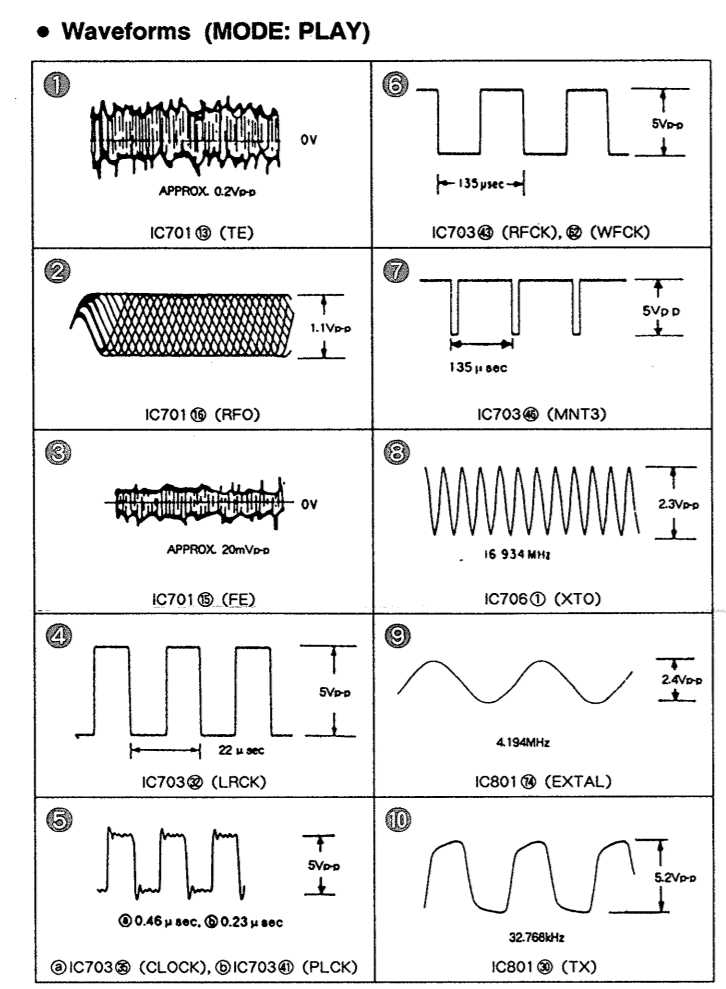
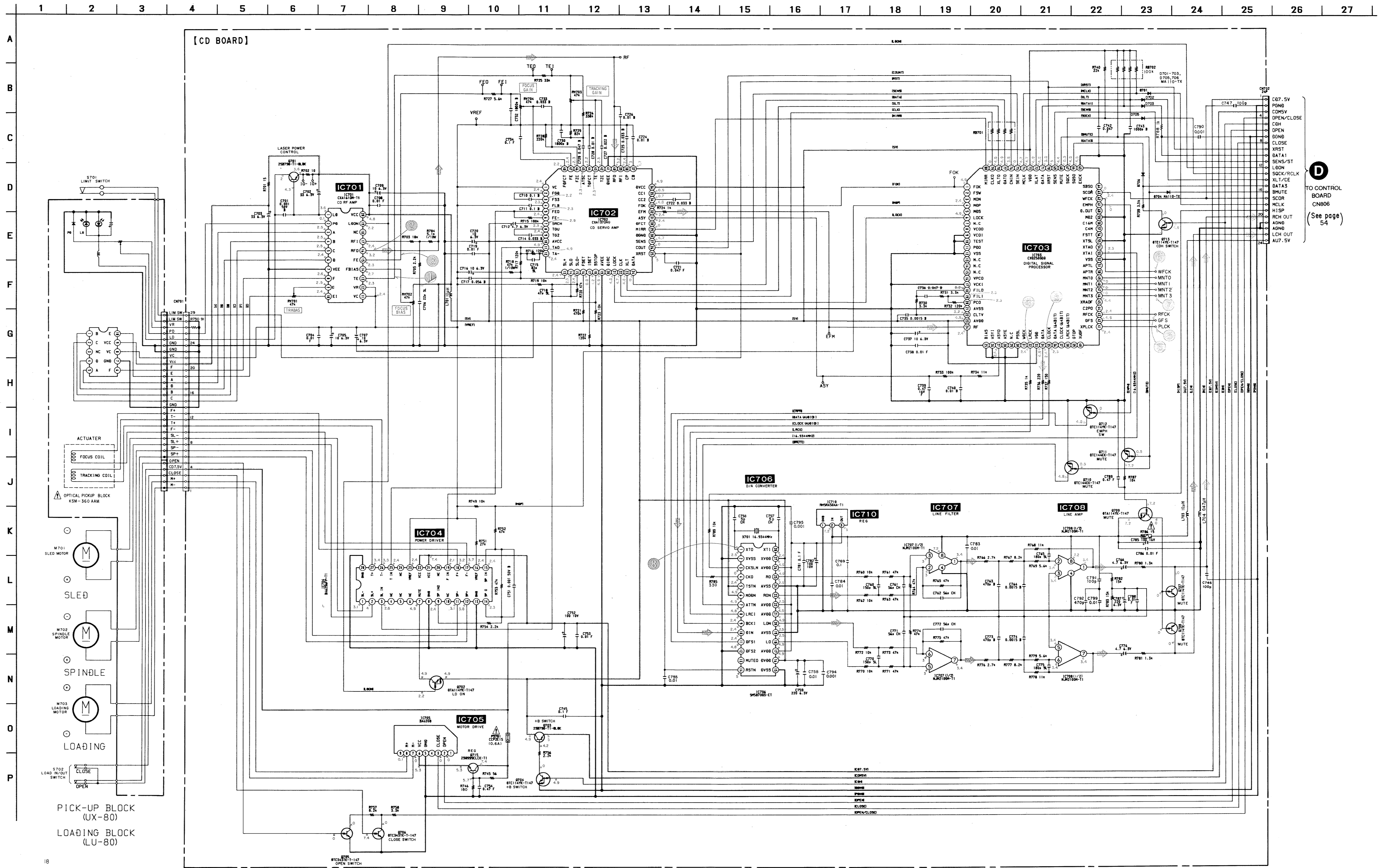


• See page 55 for IC Block Diagrams.



- Note:**
- All capacitors are in μF unless otherwise noted. pF : μF 50WV or less are not indicated except for electrolytics and tantalums.
 - All resistors are in Ω and $\frac{1}{4}\text{W}$ or less unless otherwise specified.
 - --- : B+ Line
 - --- : adjustment for repair.
 - Power voltage is dc 12 V and fed with regulated dc power supply from external BATTERY TERMINAL.
 - Voltage is dc with respect to ground under no-signal (detuned) conditions. no mark: FM (): MW (>): SW
 - Voltages are taken with a VOM (Input Impedance $10\text{M}\Omega$). Voltage variations may be noted due to normal production tolerances.
 - Signal path. --- : FM

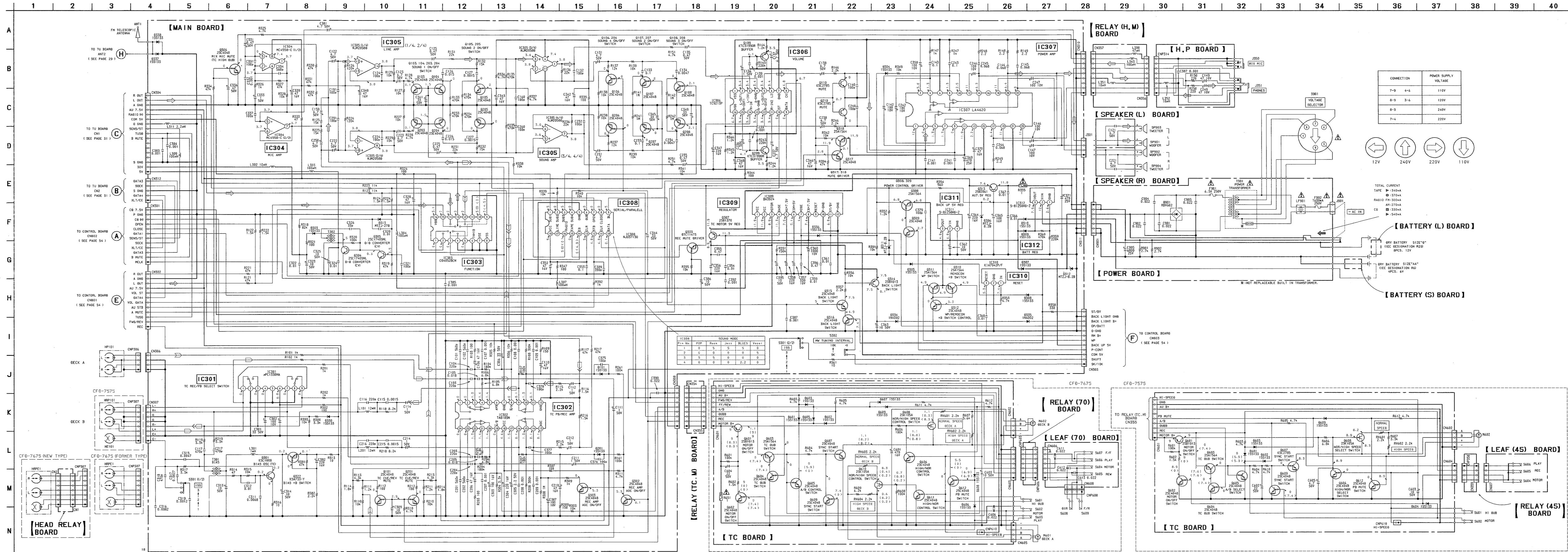
6-7. SCHEMATIC DIAGRAMS - CD Section - See page 55 for IC Block Diagrams.



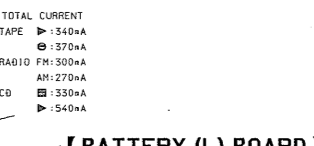
- Note: All capacitors are in μF unless otherwise noted. pF: μF 50WV or less are not indicated except for electrolytics and tantalums. All resistors are in Ω and 1/4W or less unless otherwise specified. Note: The components identified by mark A or dotted line with mark A are critical for safety. Replace only with part number specified. Legend: B+ Line, adjustment for repair, Power voltage is dc 12 V and fed with regulated dc power supply from external BATTERY TERMINAL, Voltage and waveforms are dc with respect to ground, Voltages are taken with a VOM (input impedance 10MΩ). Voltage variations may be noted due to normal production tolerances, Waveforms are taken with an oscilloscope. Voltage variations may be noted due to normal production tolerances, Circled numbers refer to waveforms, Signal path, CD

TO CONTROL BOARD CN806 (See page 54)

• See page 55 for IC Block Diagrams.



CONNECTION	POWER SUPPLY VOLTAGE
7-9 4-6	110V
8-9 3-6	120V
8-5	240V
7-4	220V



TOTAL CURRENT
 TAPE ▶ 150mA
 ◉ 375mA
 RADIO FM 300mA
 AM 275mA
 CB ▶ 350mA
 ◉ 150mA

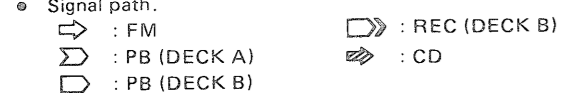
- Note:**
- All capacitors are in μF unless otherwise noted. pF : μF F
 - All resistors are in Ω and $1/4\text{W}$ or less unless otherwise specified.
 - : fusible resistor.

Note:
 The components identified by mark or dotted line with mark are critical for safety. Replace only with part number specified.

- : B+ Line
- Total current is measured with no cassette installed.
- Power voltage is dc 12 V and fcd with regulated dc power supply from external BATTERY TERMINAL.
- Voltage is dc with respect to ground under no-signal (detuned) conditions.

MAIN () : TAPE
 < > : CD
 TC () : PLAY DECK B
 < > : REC

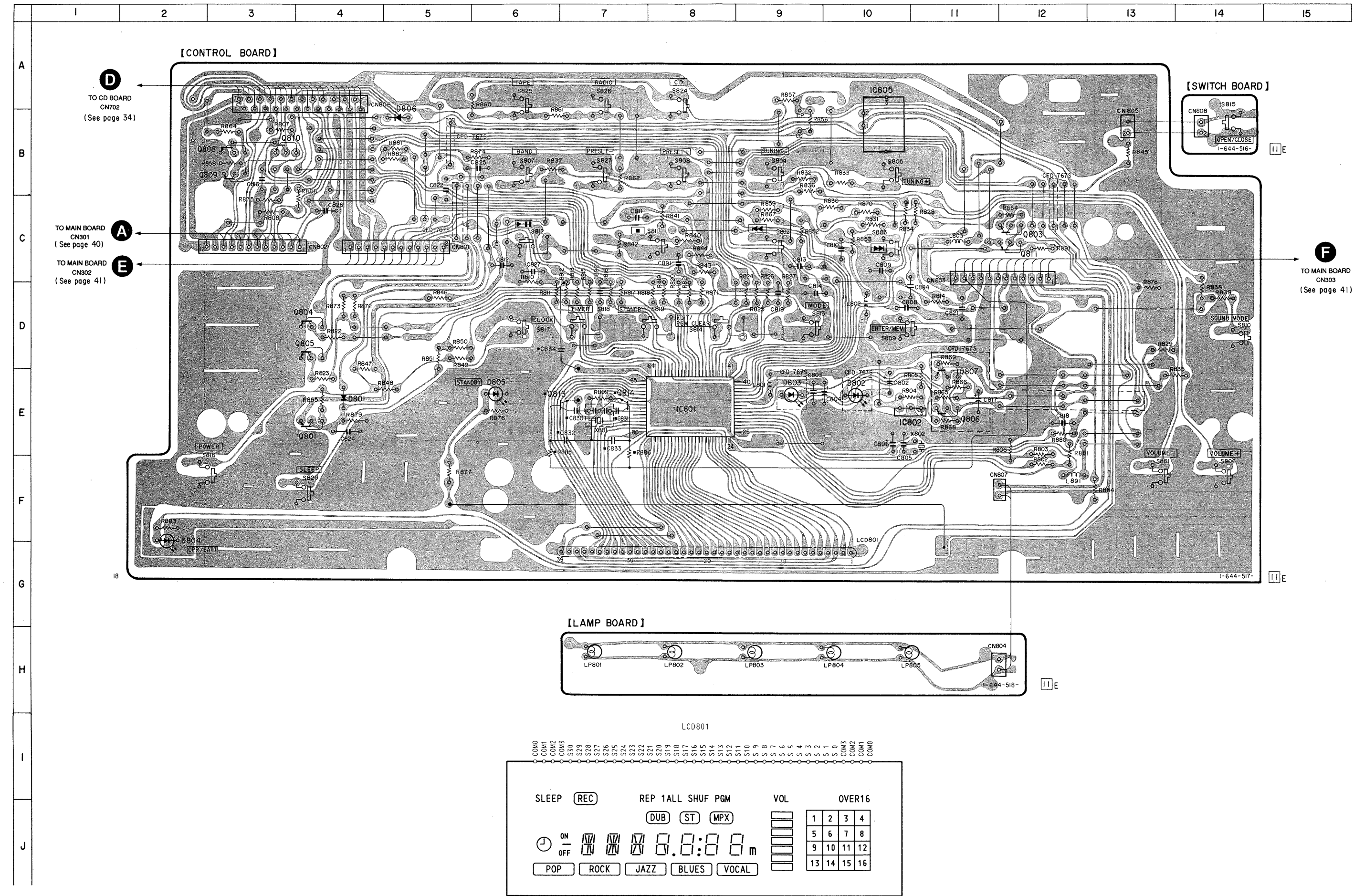
- Voltages are taken with a VOM (Input Impedance 10M Ω). Voltage variations may be noted due to normal production tolerances.



**6-10. PRINTED WIRING BOARDS
—CONTROL Section—**
• See page 25, 26 for Circuit Boards Location and Semiconductor Lead Layouts.

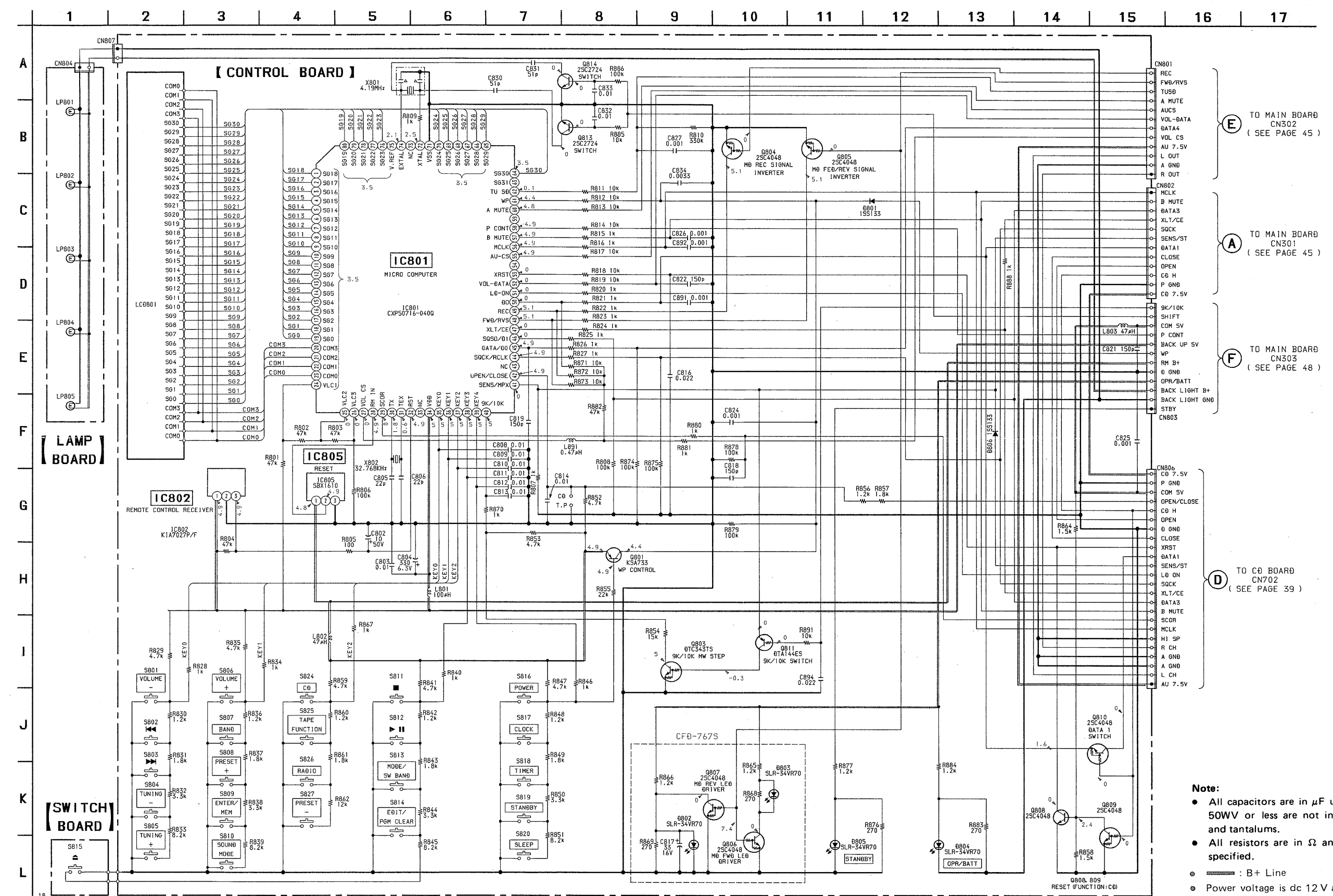
• Semiconductor Location

Ref. No.	Location
D801	E-4
D802	E-10
D803	E-9
D804	F-2
D805	E-6
D806	B-5
IC801	E-8
IC802	E-10
IC805	A-10
Q801	E-4
Q803	C-12
Q804	D-4
Q805	D-4
Q806	E-11
Q807	E-11
Q808	B-3
Q809	B-3
Q810	B-3
Q811	C-12
Q813	E-6
Q814	E-7



Note:
 • — : parts extracted from the component side.
 • — : parts extracted from the conductor side.

6-11. SCHEMATIC DIAGRAMS —CONTROL Section— • See page 55 for IC Block Diagrams.



Note:
 • All capacitors are in μF unless otherwise noted. pF: μF 50WV or less are not indicated except for electrolytics and tantalums.
 • All resistors are in Ω and $1/4\text{W}$ or less unless otherwise specified.
 • — : B+ Line
 • Power voltage is dc 12 V and fed with regulated dc power supply from external BATTERY TERMINAL.
 • Voltage is dc with respect to ground under no-signal (detuned) conditions.
 • Voltages are taken with a VOM (Input Impedance $10\text{M}\Omega$). Voltage variations may be noted due to normal production tolerances.